DESIGN BRIEFS Innovative Designs from Readers

skipping

nF will yield a starting point for testing. But final values depend on the opto selected.) While U1 is active, these four values must not allow $V_{\rm ISOL}$ to dip below the logic-high threshold of the 1-Wire device $(2.4\ V)$.

For the master to receive data from the 1-Wire device, the opto (U3) is modulated by the level of $V_{\rm ISOL}$ or the data pin of the 1-Wire device (DQ). When U1 is ON, the LED in U3 is ON. This forces Rx low.

When the 1-Wire device pulls DQ low, the LED turns off and Rx goes high. Rx also goes high when U1 turns off, causing V_{ISOL} to drop and turn off the LED. (Tx and Rx logic for the microcontroller is the inverse of that for the 1-Wire device.)

So one opto, a transformer, and a Dallas 1-Wire serial link form an isolated sensor interface between a master microcontroller and a precision 1-Wire thermometer. By keeping wiring and component costs to a minimum, the circuit supports high-volume medical and industrial applications. **ED Online 11161**

GREG SUTTERLIN, senior field applications engineer, received a BSEE as well as an MSEE from the University of Pittsburgh, Pa.

BICH PHAM, applications engineer, received a BSEE from San Jose State University, Calif.

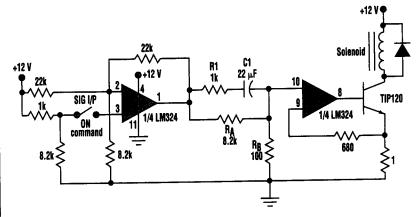
DESIGN BRIEFS WANTED

Smart Solenoid Driver Reduces Power Loss

blanking

S.V. Nakhe

nakhe@cat.ernet.in • Center for Advanced Technology, Indore, India



 ${\bf 1.}$ This smart solenoid driver circuit substantially reduces power loss while the device is held in the ON state.

Solenoids and relays used in electronic control applications consume lots of power. The current required for activation is typically three to 10 times higher than the minimum needed to hold the relay or solenoid in the ON position. Thus, reducing the driver's operating current after the solenoid or relay is switched ON will save power and reduce thermal loading. The technique presented here substantially reduces power dissipation when the device is ON.

The circuit employs an op amp to generate a constant-current sink, which uses a power transistor that drives the solenoid (Fig. 1). The reference voltage for the current sink is shaped in such a way that within tens of milliseconds after the solenoid is switched ON, the current drops and is maintained at more than (by a suitable safety margin) the minimum needed to hold the solenoid in ON state.

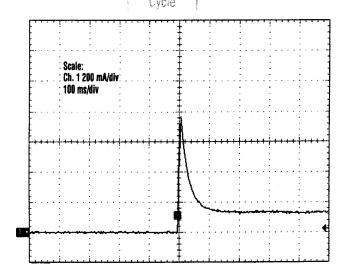
The circuit was designed for a solenoid operated from a 12-V supply. The solenoid needs around 800 mA to switch on, but it requires less than 100 mA to remain ON. The ON command input signal connects the positive supply voltage at the noninverting input of the comparator formed by part of the LM324 (pin 3). After activation, the reference point (pin 10) for the current-sink portion of the circuit (formed by part of the

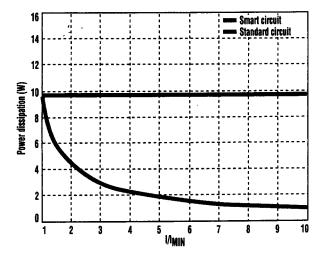
LM324) is driven high by the comparator output (pin 1 of the LM324). This will drive the transistor (TIP120) into saturation as in the standard circuit.

After a predefined time determined by the R1-C1 time constant, the reference point to the current sink settles to the final steady-state value determined by the $R_{\rm B}/R_{\rm A}$ ratio and the current-sense resistor (1 Ω). Thus, the circuit turns on the solenoid like a standard circuit (driving 760 mA into the solenoid) and later settles to the reduced operating-current mode (140 mA).

The waveform for the solenoid current monitored on the circuit is illustrated in Figure 2. The reduced ON-state current in the solenoid cuts its power dissipation. Unlike a standard circuit, additional power dissipation will exist in the driver transistor and resistor at the emitter of the transistor. However, the total power dissipated in the circuit will be substantially lower.

The power dissipated in the solenoid is reduced by the square of the current reduction ($P = I^2R$), while the major part of the additional power dissipation in the driver transistor follows a linear relationship: P = VI. The calculated power dissipated in a standard circuit and the power dissipated in the smart circuit (including





blanking

2. When the solenoid current is monitored, it produces the waveform as shown.

This graph compares the solenoid's ON-state power dissipation when operated in a standard circuit with that obtained using the smart driver circuit.

the power dissipation in the solenoid, the driver transistor, and current-sense resistor) is up to 10 times lower than the activation current for a solenoid operating from a 12-V supply and drawing 800 mA of current (Fig. 3).

Csc ii atei

This circuit can be easily adapted to drive relays or solenoids operated from other voltage levels. Considering the increased power dissipation in the driver transistor, a suitable heatsink should be used with it. **ED Online 11162**

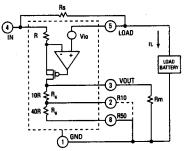
S.V. NAKHE, scientific officer with the Center for Advanced Technology in Indore, India, received a BE from the Walchand College of Engineering, Sangli, Shivaji University, India. He also received an MS in engineering from the Indian Institute of Science, Bangalore, India

Accurate, Programmable High Side Current Measurement

IXI848/IXI848A High Side Current Monitor



IXI848 Application Circuit



Features

- · High Side Current Sense Amplifier
- 2.7V to 40V Input Range IXI848
- 2.7V to 60V Input Range IXI848A
- ± 0.7% Typical Full Scale Accuracy
- User Selectable 10x / 50x Gain
- Oser Selectable low/ Sox Com
- -40°C to +85°C Operating Ambient Temperature Range
- ± 400mV Differential Input Voltage (VSENSE)

Applications

- Power Management Systems
- Smart Battery Packs
- Battery Chargers
- Battery Powered Portable Equipment
- DC Motor Control

www.ixys.com

1 GND Ground 2 R10 Connecting R10 to GND, (R50=N/C) selects a VOUT voltage that is 10X the voltage across current sense resistor 3 VOUT Output voltage proportional to the voltage across current sense resistor Rs.	
3 VOUT Output voltage proportional to the voltage across current sense resistor Rs.	
	Rs.
The state of the s	
4 IN Positive supply terminal and power connection for the external sense resistor Rs.	
5 LOAD High impedance load-side connection to the external sense resistor Rs.	
8 R50 Connecting R50 to GND, (R10=N/C) selects a VOUT voltage that is 50X the voltage across current sense resistor	Rs.
6,7 N/C No Connection.	

Efficiency Through Technology

